

WEST Search History

DATE: Sunday, August 01, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L6	l2 not l3	9
<input type="checkbox"/>	L5	(multiple interface or multi\$1interface) near3 (\$processor or cpu)	11
<input type="checkbox"/>	L4	5930523[uref]	50
<input type="checkbox"/>	L3	L1.ti.	12
<input type="checkbox"/>	L2	L1.ti,ab.	21
<input type="checkbox"/>	L1	(multiple bus or multi\$1bus) near3 (\$processor or cpu)	379

END OF SEARCH HISTORY

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WEST Search History

DATE: Monday, August 02, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L14	5890005[uref]	8
<input type="checkbox"/>	L13	5890005.pn.	1
<input type="checkbox"/>	L12	4228498[uref]	16
<input type="checkbox"/>	L11	4228498[uref]	16
<input type="checkbox"/>	L10	4228498[uref]	16
<input type="checkbox"/>	L9	L8.ti.	7
<input type="checkbox"/>	L8	(\$processor or cpu) near3 multi\$bus	181
<input type="checkbox"/>	L7	6377581[uref]	1
<input type="checkbox"/>	L6	(\$processor or cpu) near3 interface near5 (point\$point)	21
<input type="checkbox"/>	L5	L3 near5 (plurality or several or multiple)	12
<input type="checkbox"/>	L4	L3.ti.	1
<input type="checkbox"/>	L3	(\$processor or cpu) near3 different near3 interface	219
<i>DB=TDBD; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L2	multiple microprocessor interfaces	1
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<input type="checkbox"/>	L1	(\$processor or cpu) near3 multiple interface	38

END OF SEARCH HISTORY

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